## REMARKS

The Office Action dated April 11, 2005 has been carefully considered. Claims 1-17 are pending. The above amendments and the following remarks are presented in a sincere attempt to place this Application in condition for allowance. Claims 1, 3-5, 9 and 12-17 have been amended in this Response. Reconsideration and allowance are respectfully requested in light of the above amendments and the following remarks.

An interview was held with the Examiner, Mr. Dinh T. Le, on June 2, 2005, to discuss the rejections under 35 U.S.C. § 102(b) and the proposed amendments thereto. Applicants wish to thank the Examiner for his time and the courtesies extended.

Claims 1-17 stand rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which Applicants regard as the invention. Insofar as these rejections may be applied against the amended claims, they are deemed overcome.

Claims 1, 3-5, 8, 13-14 and 16-17 have been amended to provide clear antecedent basis. Claim 9 has been amended to describe "delaying the voltage level *through the use of at least one latch* [and] detecting an edge on the voltage level *by an edge detector*." These amendments clarify the limitations of Claim 9.

The "increment value" of Claim 10 represents a "second clock frequency" because each increment value that is produced and stored represents a change in voltage, which can be described as a clock signal. Therefore, the multiple increment values that are stored imply multiple changes in voltage and provide a "second clock frequency." In Claim 11, "the voltage level is inverted" by switching a high voltage level to a low voltage level or switching a low voltage level to a high voltage level.

In Claim 13 the "output of the memory" provides a signal that is input into an incrementer. In response to this signal, the incrementer changes a previous value into "a next value." Claim 14 has been amended to describe "the voltage level" as claimed in Claim 9. In Claim 15 the "incrementer employs an n-bit adder" to increment a value as described in Claim 9. Claim 16 has been amended to describe "a clock frequency *from the clock pulse* and a core mesh-clock frequency *from the increment value*." Claim 17 has been amended to state "an enable signal is conveyed to *a* clock *for generating the clock pulse*." Accordingly, Applicants respectfully request that the rejections of Claims 1-17 under 35 U.S.C. § 112, second paragraph, be withdrawn and that Claims 1-17 be allowed.

Claims 9-10 and 16-17 stand rejected under 35 U.S.C. § 102(b) in view of U.S. Patent 5,982,833 to Waters ("Waters"). Insofar as these rejections may be applied against the amended claims, they are deemed overcome.

Claim 9 has been amended to include a distinguishing feature of the present invention. The method of Claim 9 describes "incrementing a value based upon an edge detection, *and storing an ncrement value*." Support for this amendment can be found, among other places, FIG. 1 of the original Application.

The Waters reference does not teach, suggest or disclose this feature of the present invention. Specifically, Waters discloses a numerically controlled oscillator ("NCO"), which comprises a low frequency portion and a high frequency portion to output a compensation clock. In contrast with Waters, the present invention produces a clock-mesh signal that is a transformation of a generated clock signal. Time base logic receives the generated clock signal and transforms the signal to operate at a different frequency. By detecting an edge of the clock signal, incrementing a value, and storing the value, the present invention produces the clock-mesh signal which is a

transformation of the clock signal. Accordingly, the stored increment value is utilized to increment the subsequent values and produce a unique signal. This enables the clock-mesh signal to become independent of the generated clock signal and operate at a frequency which is unrelated to the generated clock signal. The ability of the present invention to produce a clock-mesh signal that is unrelated to the generated clock signal is not disclosed in Waters.

In view of the foregoing, it is apparent that the cited reference does not disclose, teach, or suggest the unique combination now recited in amended Claim 9. Applicants therefore submit that amended Claim 9 is both clearly and precisely distinguishable over the cited reference in a patentable sense. Accordingly, Applicants respectfully request that the rejection of Claim 9 under 35 U.S.C. § 102(b) in view of Waters be withdrawn and that amended Claim 9 be allowed.

Claims 10 and 16-17 depend upon and further limit amended Claim 9. Hence, for at least the aforementioned reasons, these Claims should be deemed to be in condition for allowance. Accordingly, Applicants respectfully request that the rejections of dependent Claims 10 and 16-17 also be withdrawn.

Claim 14 stands rejected under 35 U.S.C. § 103(a) in view of Waters. Insofar as this rejection may be applied against the amended claims, it is deemed overcome. Claim 14 depends upon and further limits amended Claim 9. Hence, for at least the aforementioned reasons, this claim should be deemed to be in condition for allowance. Accordingly, Applicants respectfully request that the rejection of dependent Claim 14 also be withdrawn.

Applicants have now made an earnest attempt to place this Application in condition for allowance. For the foregoing reasons and for other reasons clearly apparent, Applicants respectfully request full allowance of Claims 1-17.

Applicants do not believe that any fees are due; however, in the event that any fees are due, the Commissioner is hereby authorized to charge any required fees due (other than issue fees), and to credit any overpayment made, in connection with the filing of this paper to Deposit Account No. 50-0605 of CARR LLP.

Should the Examiner deem that any further amendment is desirable to place this application in condition for allowance, the Examiner is invited to telephone the undersigned at the number listed below.

Respectfully submitted,

CARR LLP

Dated: 2/8/05

CARR LLP
670 Founders Square
900 Jackson Street
Dallas, Texas 75202
Telephone: (214) 760-3030
Fax: (214) 760-3003

Gregory W. Carr
Reg. No. 31,093